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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2811

AF/2800
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

O I P E
JAN 14 2002
PATENT & TRADEMARK OFFICE

In re Application of

Atty. Docket

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#7

MAVEED MAJID ET AL

PHA 23,843

Serial No. 09\440,595

Group Art Unit: 2811

Filed: November 15, 1999

Examiner: N. Parekh

Title: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN
POWER APPLICATIONS

Commissioner for Patents
Washington, D.C. 20231

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NOTICE OF APPEAL

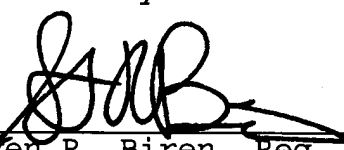
Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated July 24, 2001 of the Examiner finally rejecting claims 1-7.

- [X] Please charge the fee of \$320.00 to Deposit Account No. 14-1270.
- [] No additional fee is required, because the fee was paid in a prior appeal.

Respectfully submitted,

01/15/2002 CNGUYEN 00000002 141270 09440595
01 FC:119 320.00 CH

By 
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CERTIFICATE OF MAILING

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